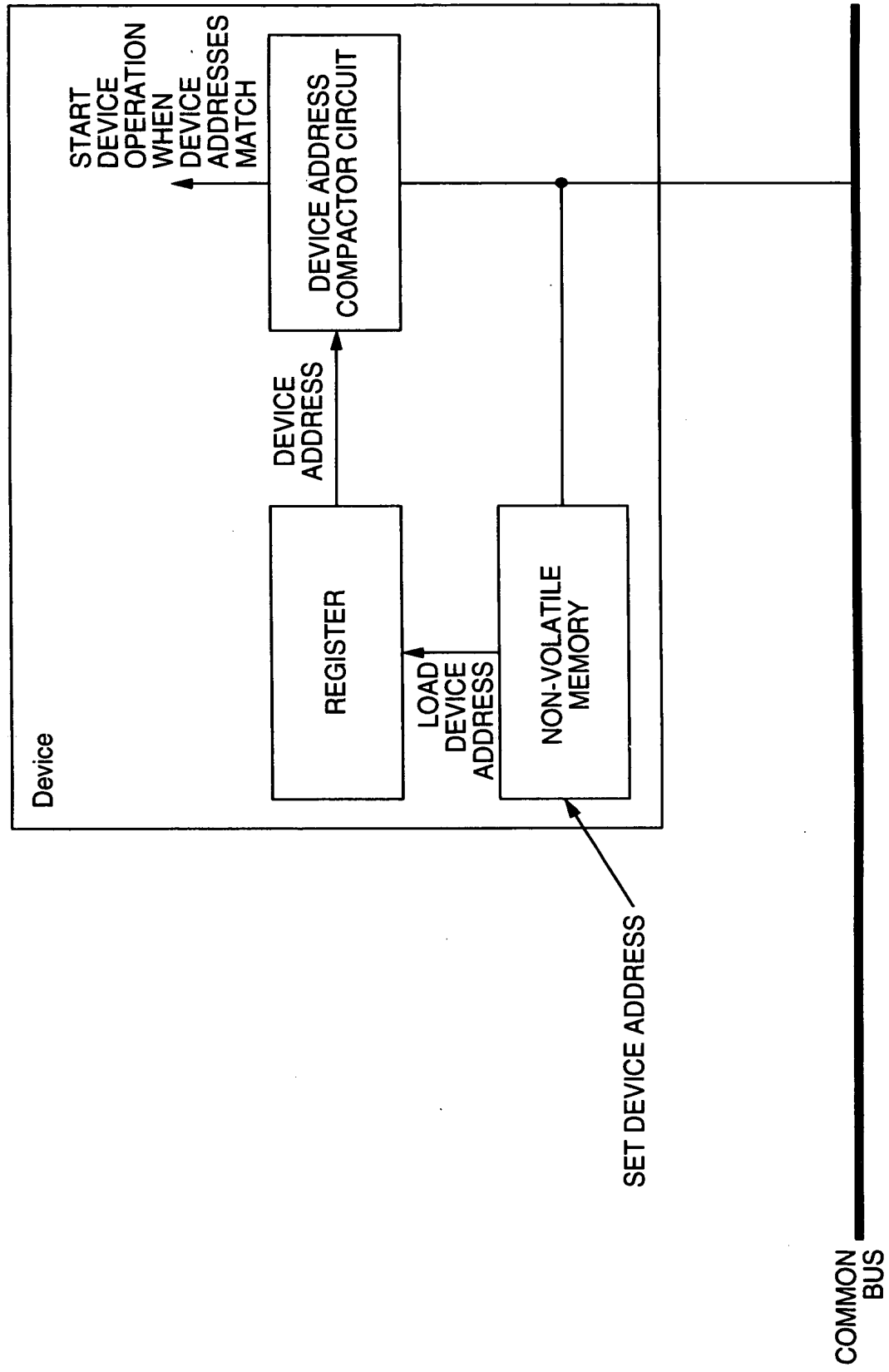


FIG. 1



434324" 4E0000

FIG. 2

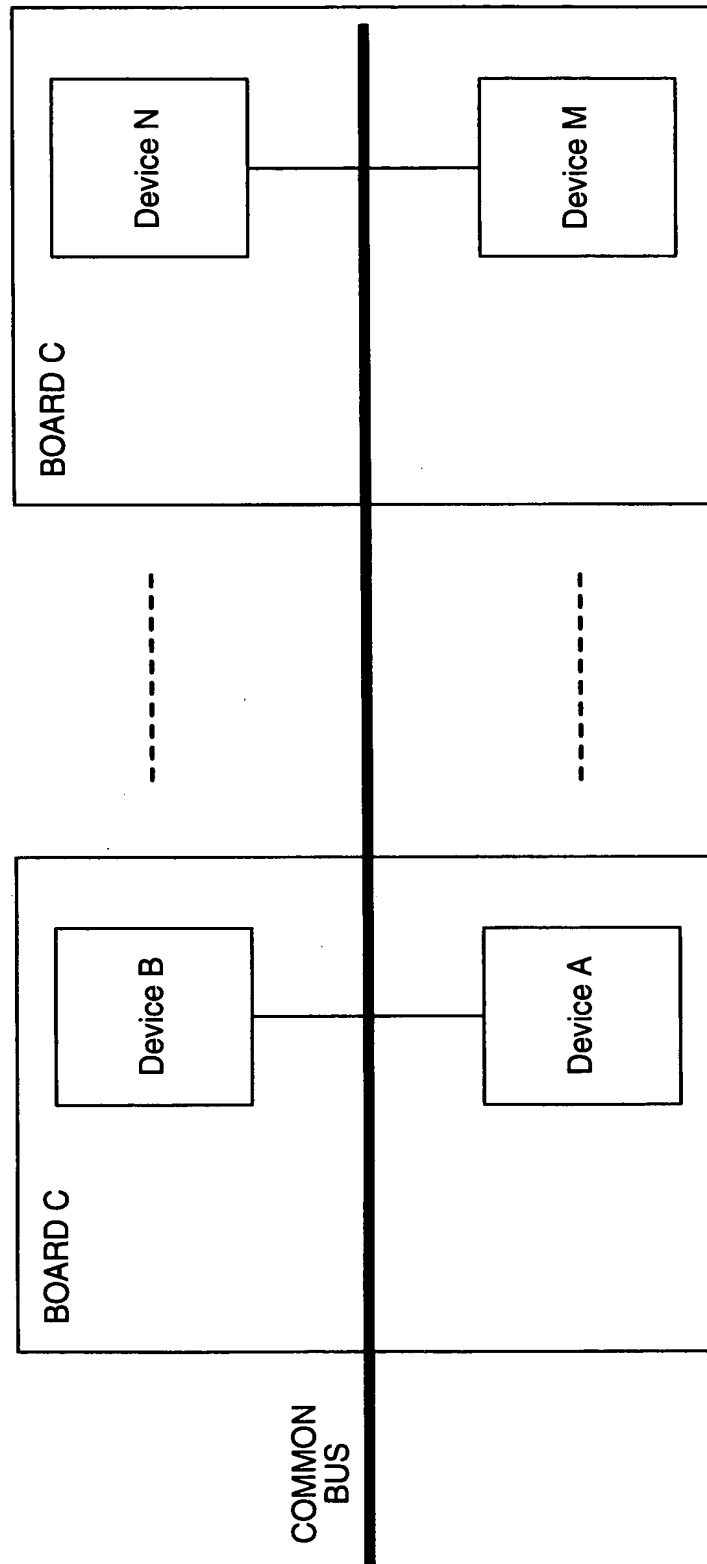


FIG. 3

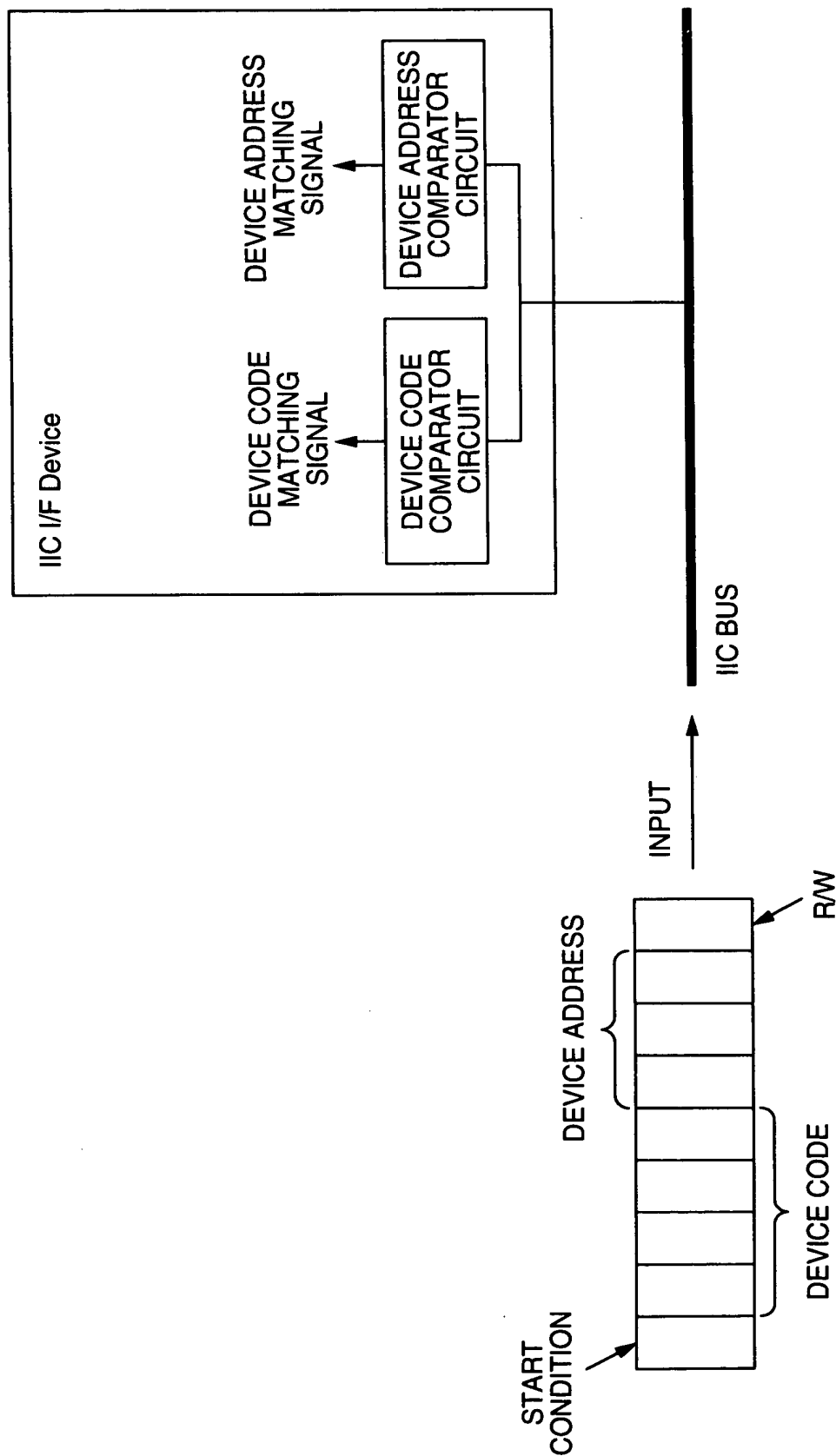


FIG. 4

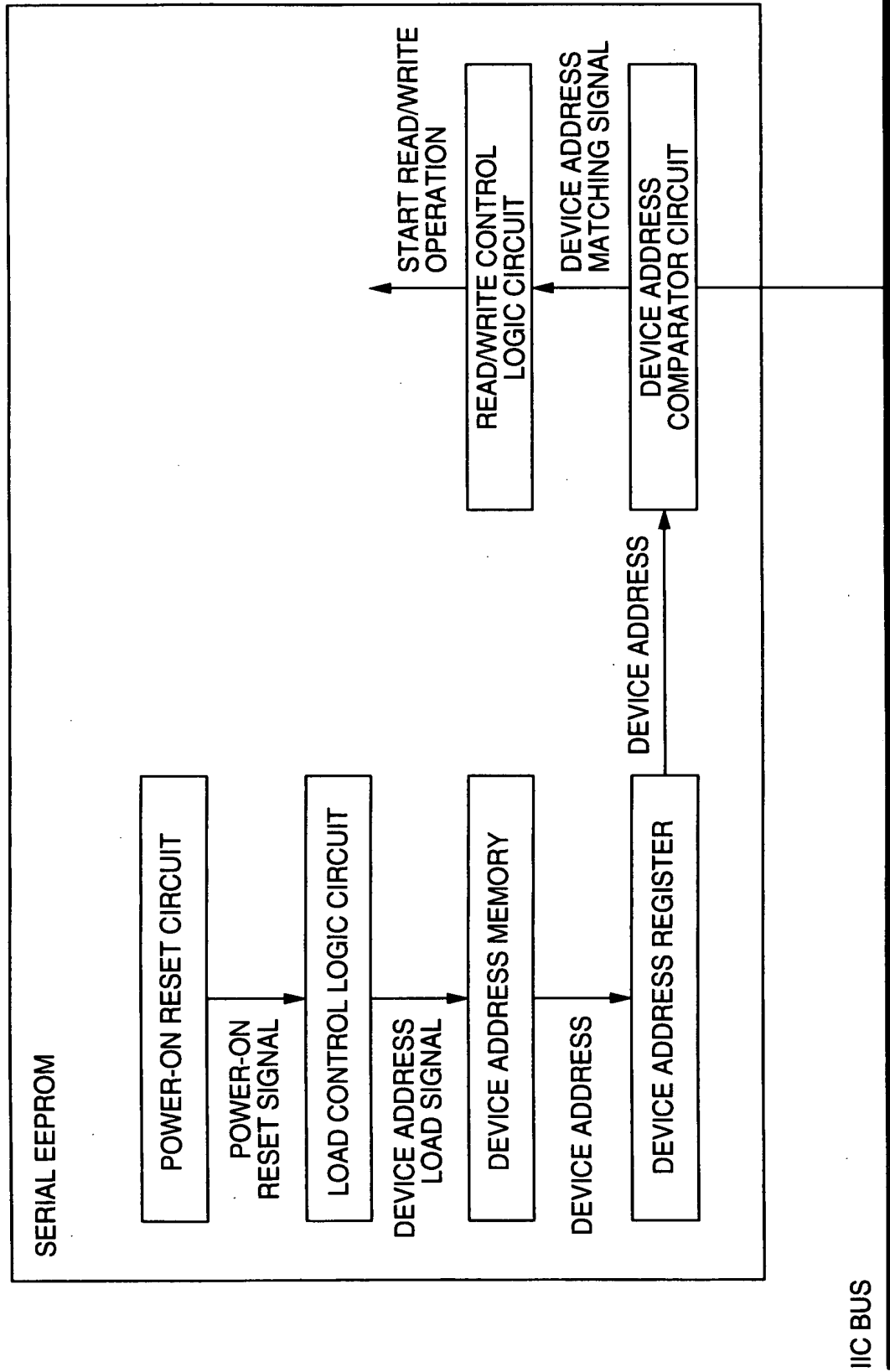


FIG. 5

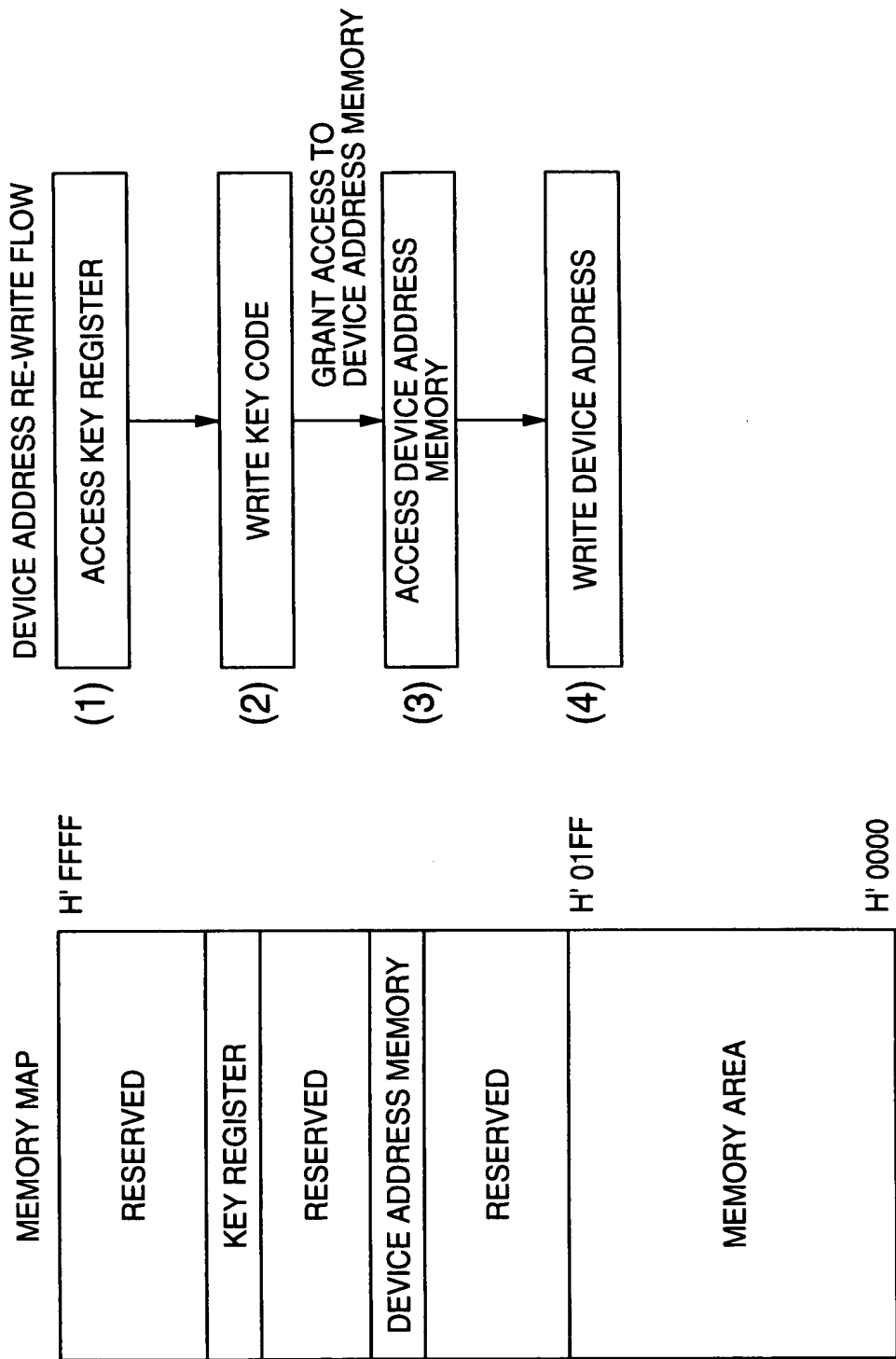
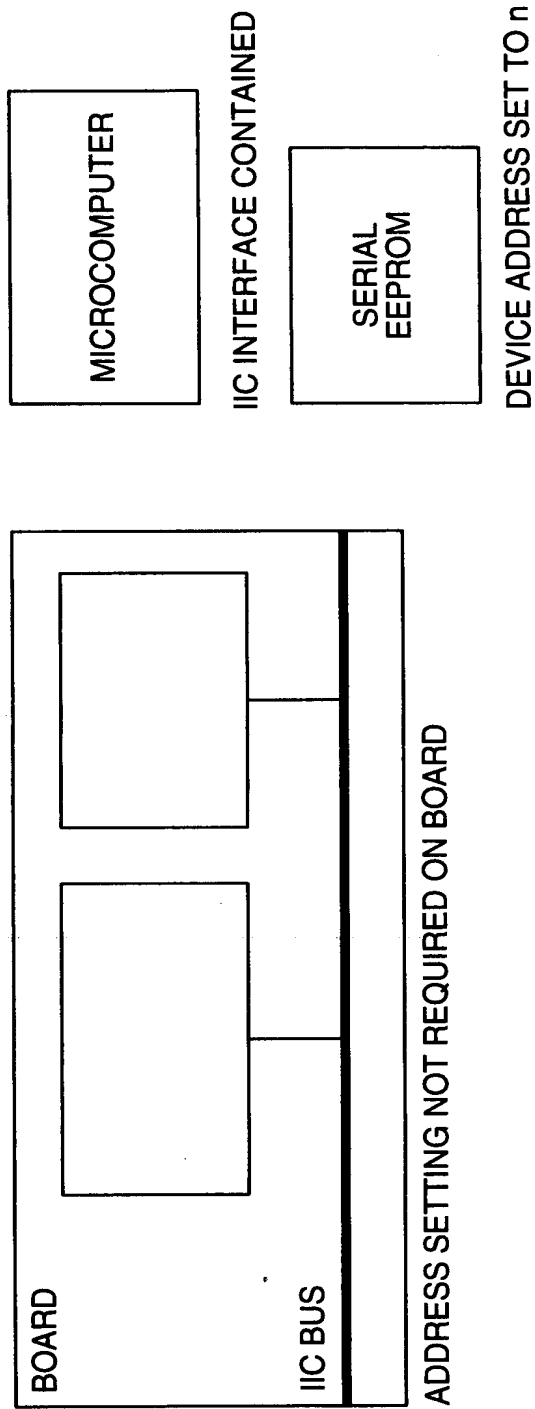


FIG. 6



38334-38343

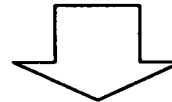
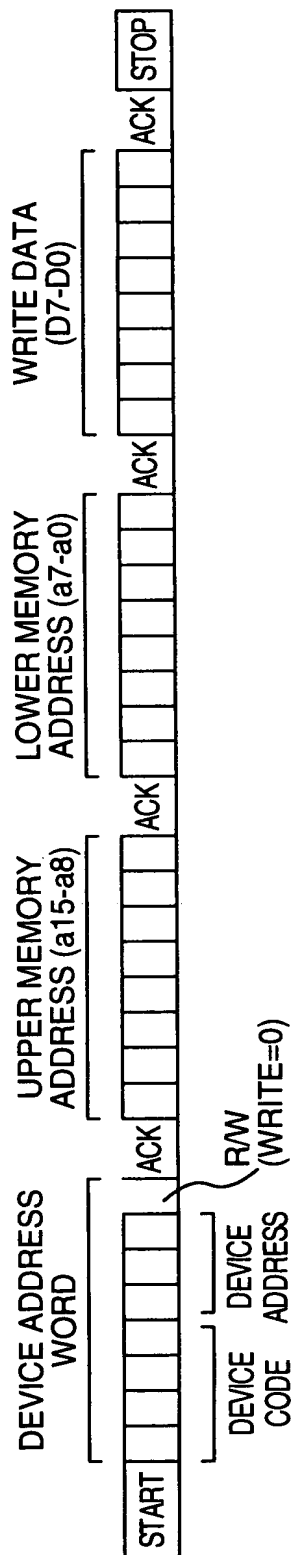
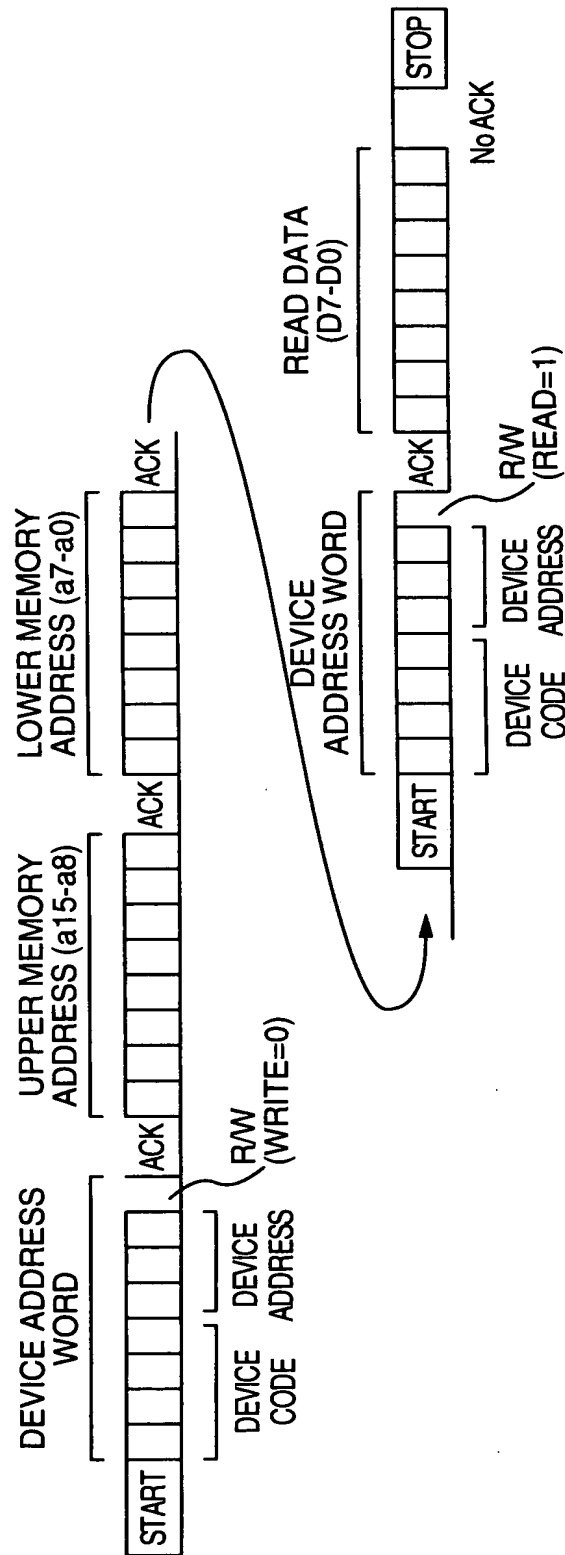


FIG. 8

COMMUNICATION PROTOCOL (FOR WRITE OPERATION)

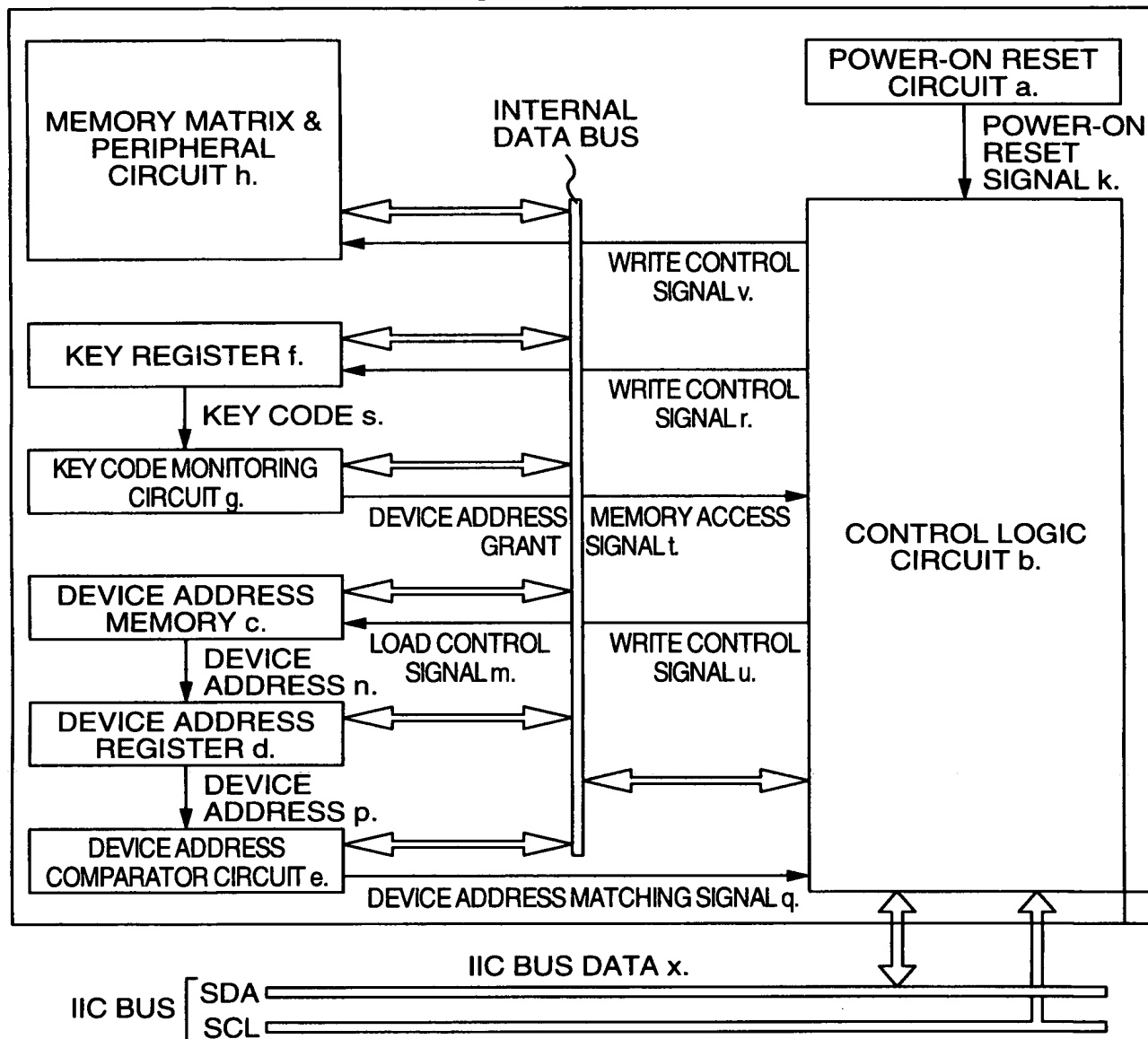


COMMUNICATION PROTOCOL (FOR READ OPERATION)





**FIG. 9**  
SERIAL EEPROM



IIC BUS DATA x.	DEVICE ADDRESS WORD*	UPPER MEMORY ADDRESS	LOWER MEMORY ADDRESS	WRITE DATA
1	10100000	H' FF	H' 10	KEY CODE
2	10100000	H' FF	H' 09	xxxxx101
3	10101010	H' 00	H' 00	WRITE DATA

**FIG.10**  
SERIAL EEPROM

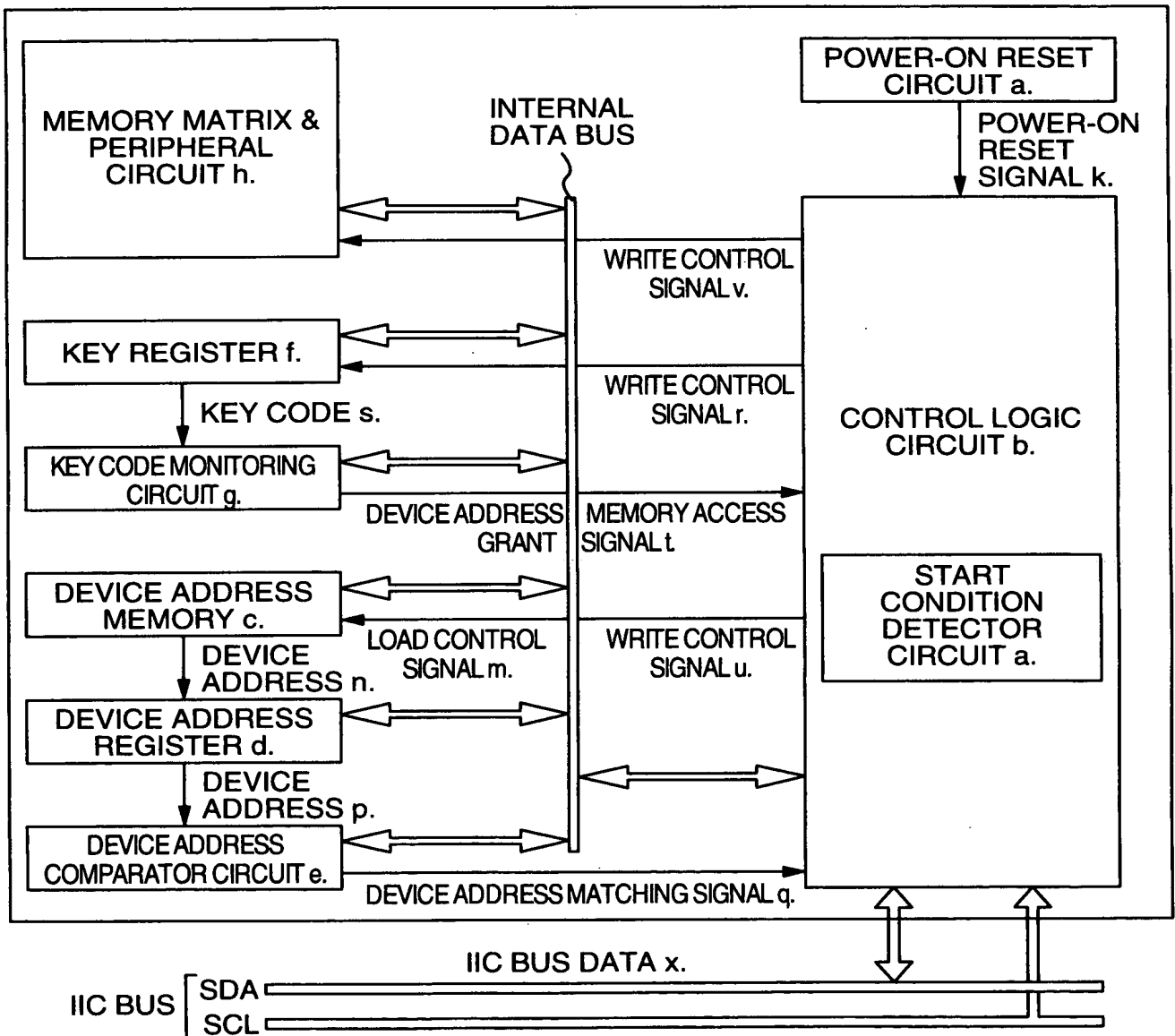


FIG. 11

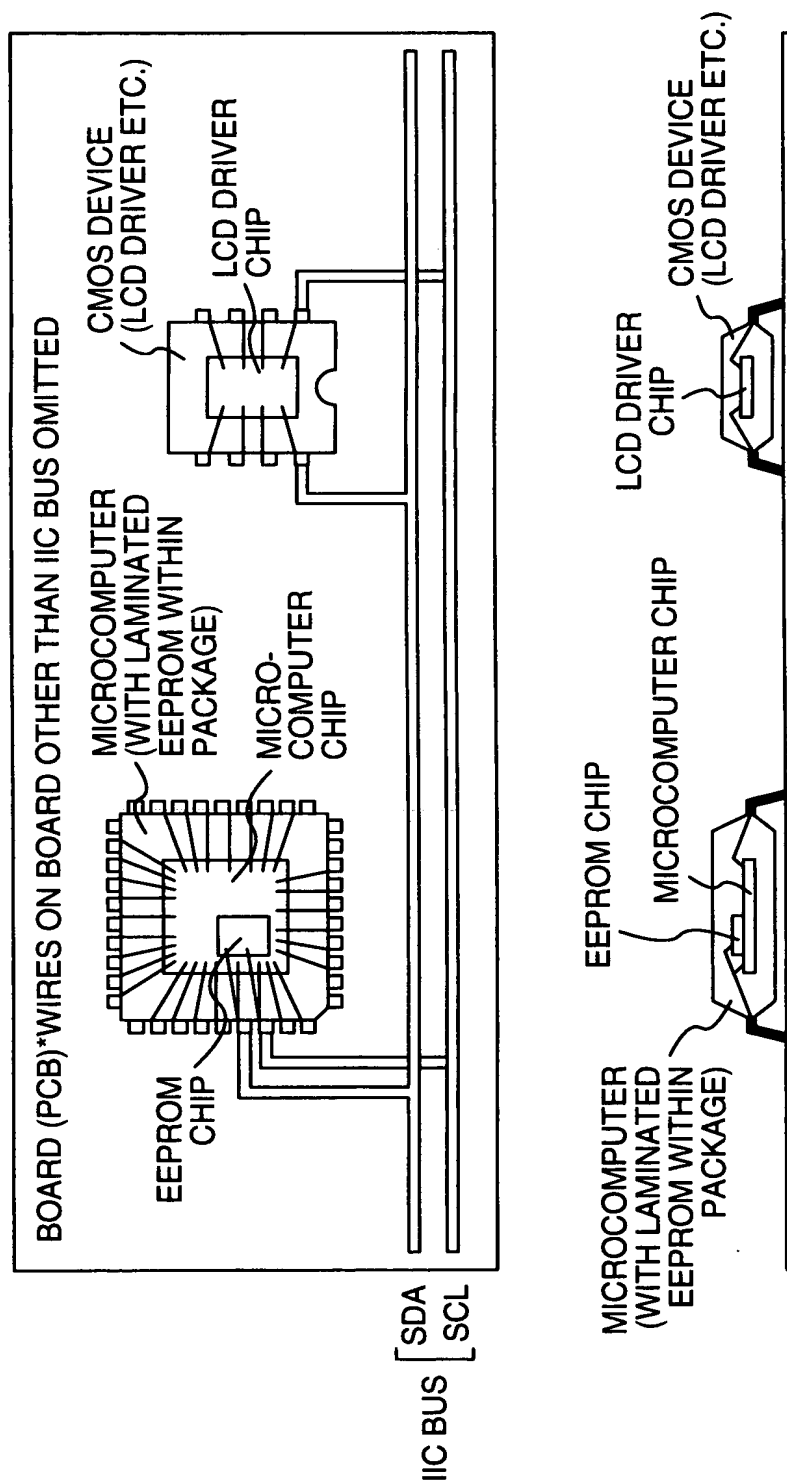
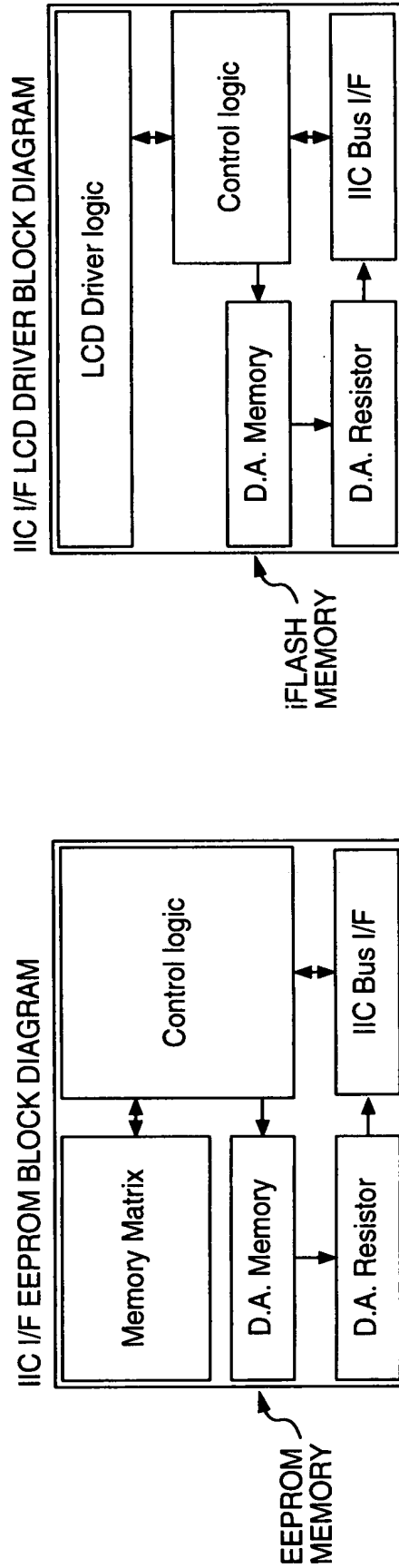


FIG. 12



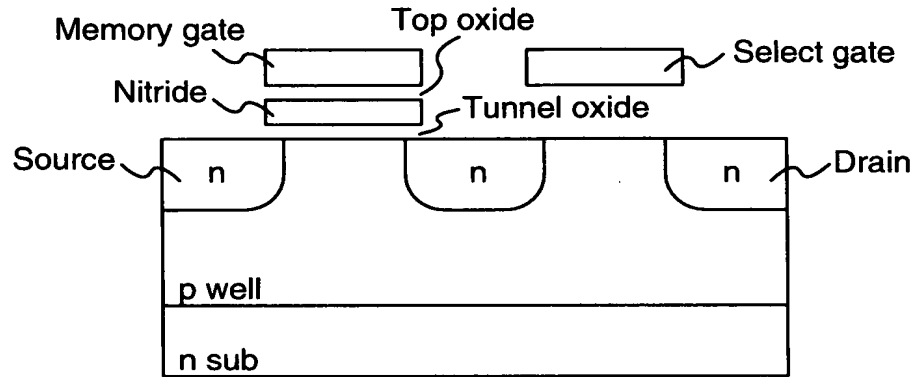
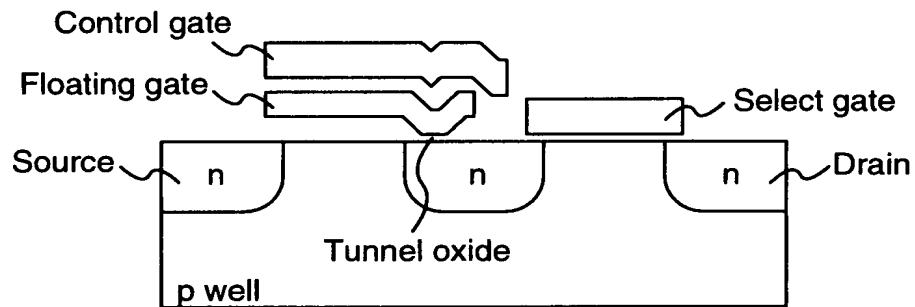
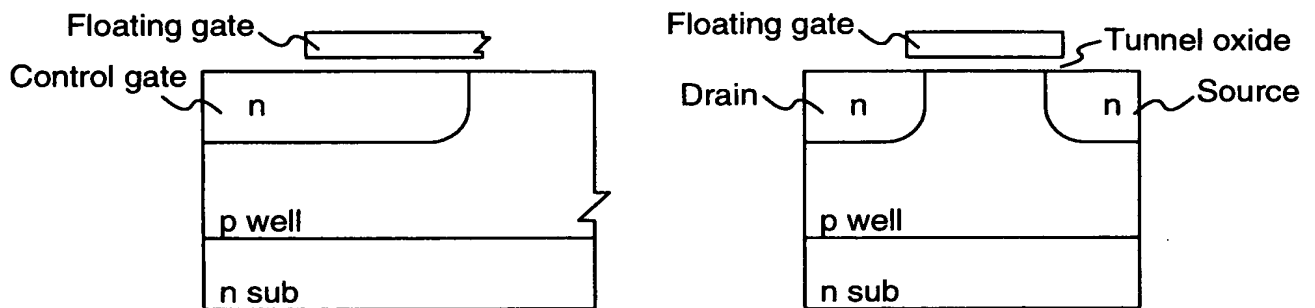
**FIG. 13****MONOS MEMORY CELL****FLOTOX MEMORY CELL****iFLASH MEMORY CELL**

FIG. 14

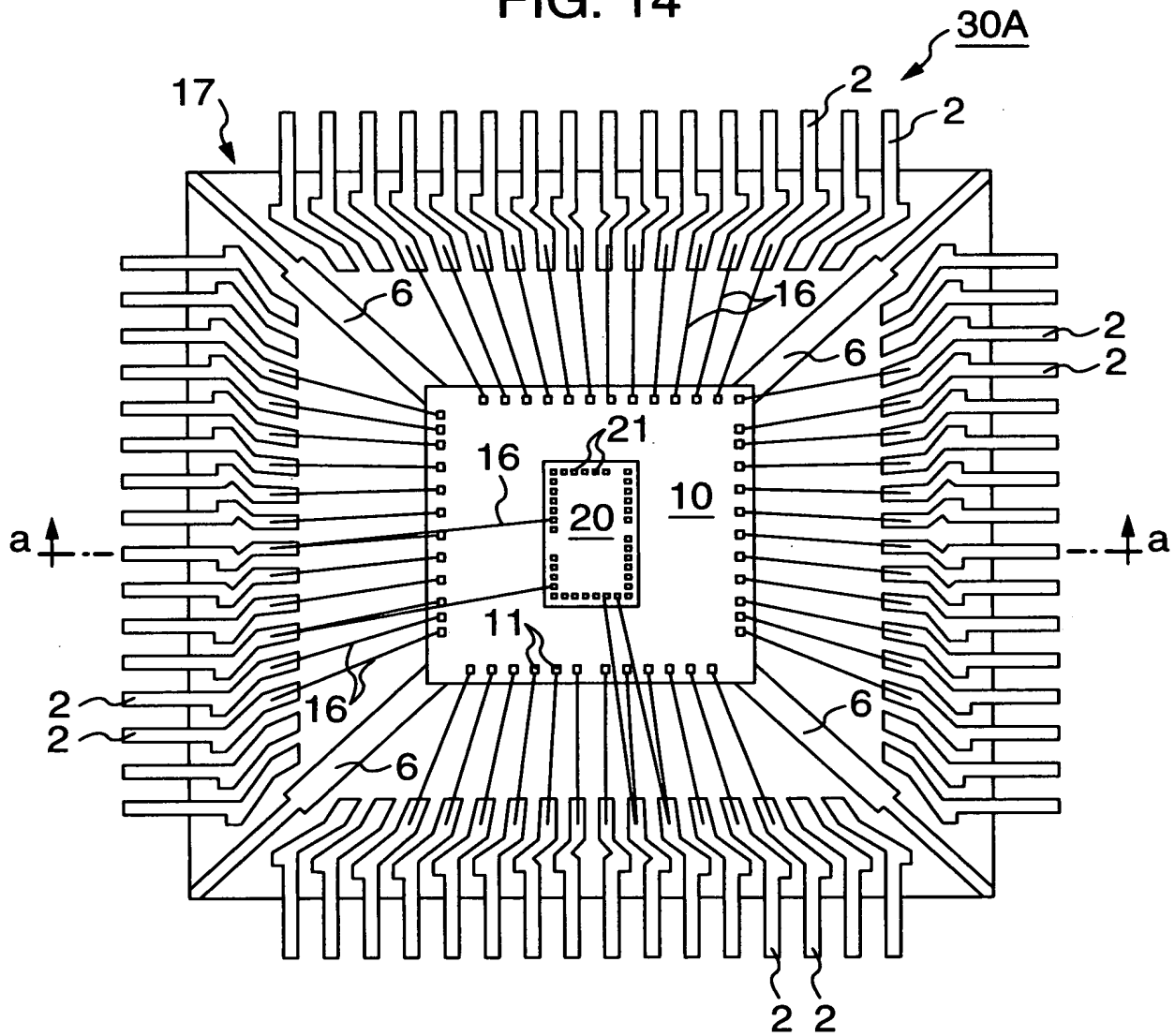


FIG. 15

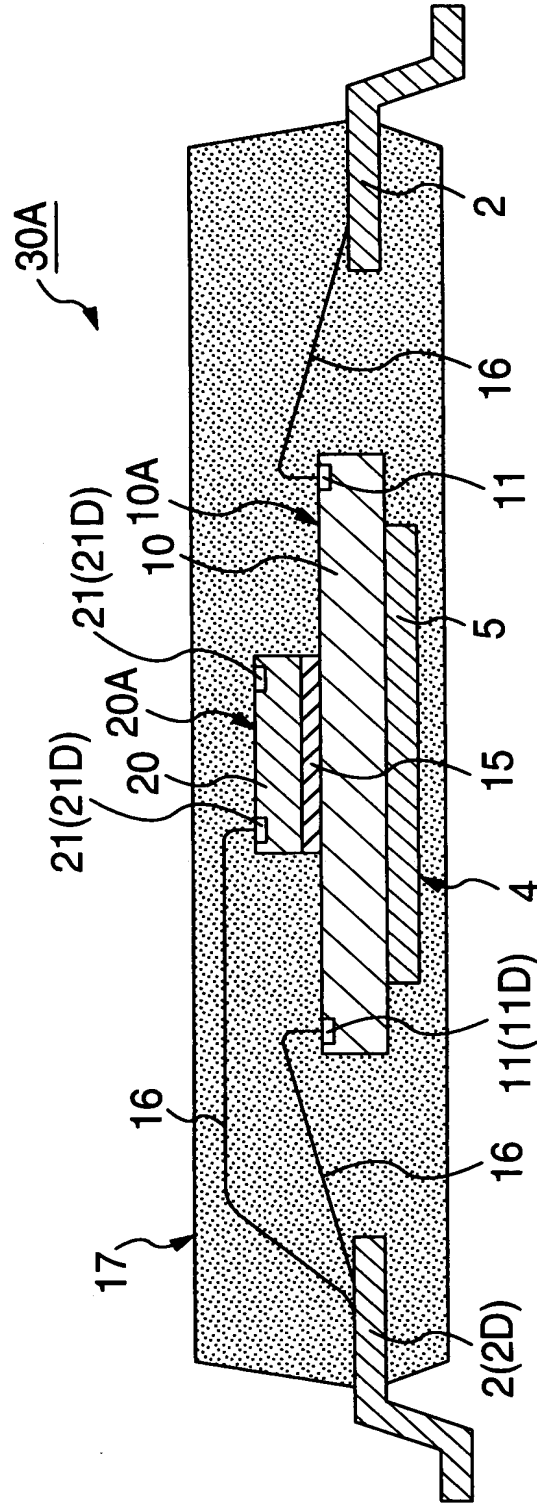
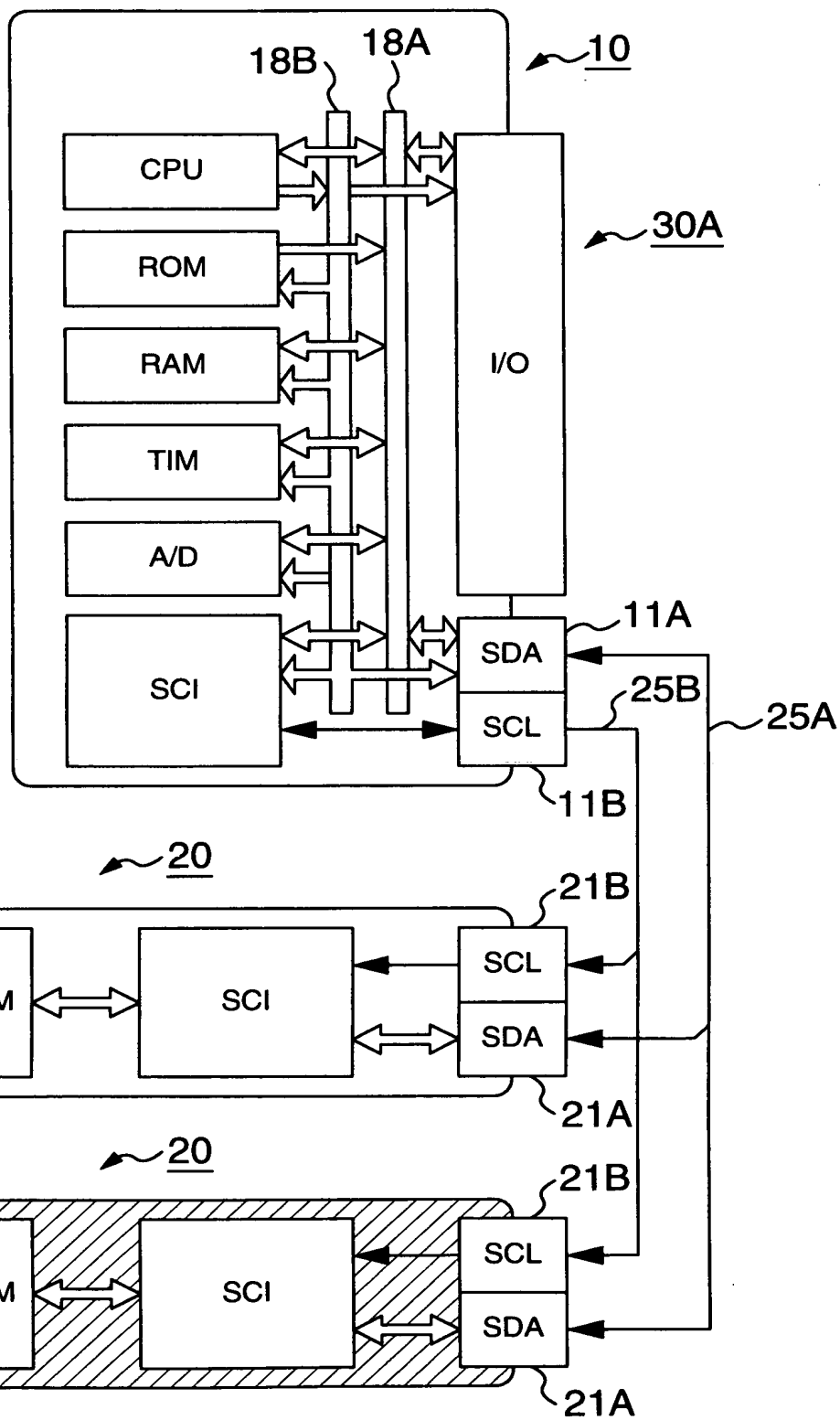


FIG. 16

<MICROCOMPUTER>



10000004 10000004



FIG. 17

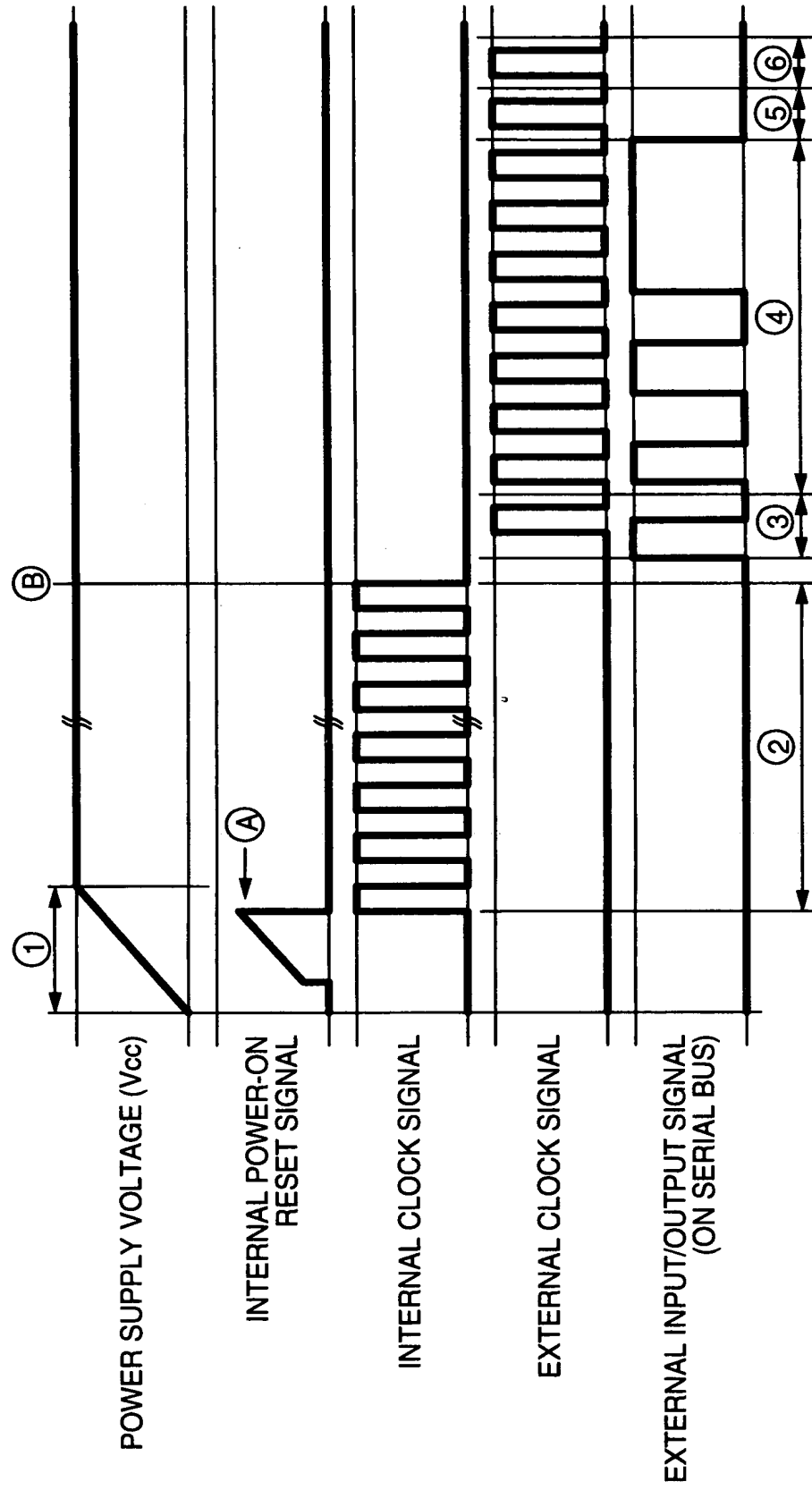


FIG. 18

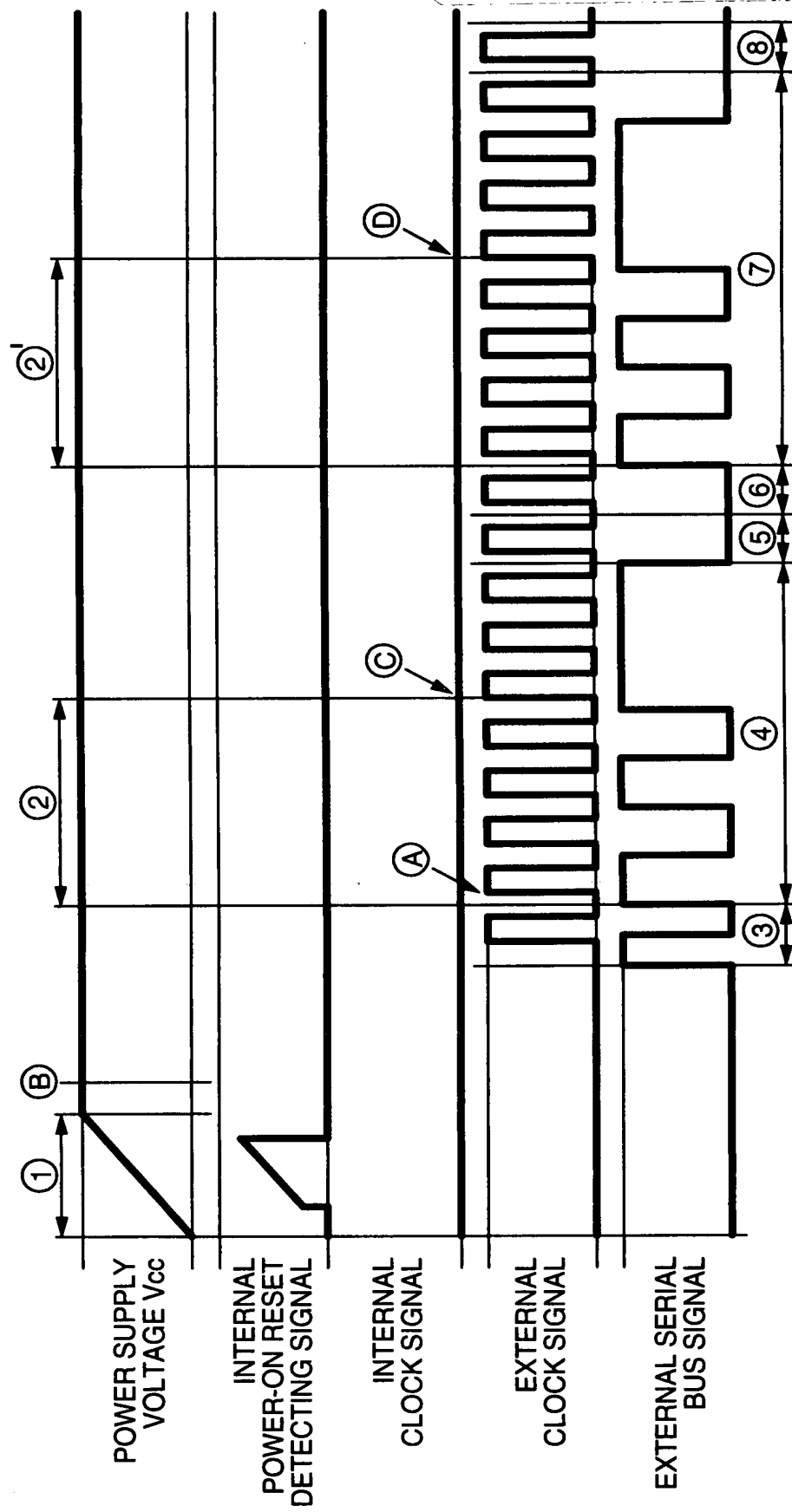


FIG. 19

